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JCS 979 U. S. PRO
09/017107

		Subclass	ISSUE CLASSIFICATION
	Class		

PATENT NUMBER

U.S. UTILITY Patent Application

<u>M.R</u>	O.I.P.E.	PATENT DATE
SCANNED	NSF Q.A. <u>AUG 4</u>	

APPLICATION NO. 09/817107	CONT/PRIOR F	CLASS 257	SUBCLASS 48	ART UNIT 281 2822	EXAMINER V/Farce
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APPlicants

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Integrated circuit provided with a substrate and with a memory, transponder, and method of programming a memory

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ___ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

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